EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	"20030131331"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/02/14 10:48
L2	15470	(crossbar or switch) with (select near2 signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/02/14 10:49
L3	64	CAM same I2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/02/14 11:02
L4	16	(("20020126672") or ("5619713") or ("6671771") or ("5809330") or ("20030131331") or ("6169685") or ("20010048625") or ("6374326")). PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/14 12:05
L5	21	(multiplexed address data bus) with (save or reduce)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/02/14 12:07
L6	68	(CAM or ((content addres$5) or associative) adj2 memory) same I2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/02/14 12:09
L7	394	(CAM or ((content addres$5) or associative) adj2 memory) and I2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/02/14 13:32
L8	9633	711/108 or 365/49 or 370/392	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/02/14 12:10

L9	20	I7 and I8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/02/14 14:11
L10	14065	I2 not mask$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/02/14 12:20
L11	326	I7 and I10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/02/14 12:20
L12	195	I11 and @ad<="20011201"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/02/14 13:37
L13	218444	"711"/$.ccls. or "365"/$.ccls. or "370"/$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/02/14 12:21
L14	32	I12 and I13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/02/14 12:22
L15	756456	(CAM or ((content addres$5) or associative) adj2 memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/02/14 13:32
L16	50897	program near2 (circuit$4 or logic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/02/14 13:35

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L17	168334	(pars$3 or filter or select) near2 (circuit$4 or logic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/02/14 13:35
L18	540	l15 and l16 and l17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/02/14 13:35
L19	13	l18 and l2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/02/14 13:36
L20	107	l18 and l13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/02/14 13:36
L21	61	l20 and @ad<="20011201"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/02/14 13:37
L22	5082	(configurable or programmable) same (decoder or selector or switch or crossbar or (cross adj2 point) or interconnect) same (control signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/02/14 14:14
L23	252	l22 and l15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/02/14 14:15
L24	67	l23 and l13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/02/14 14:15

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The **"AND"** operator is unnecessary -- we include all search terms by default. [details]

Patent Search Patents 1 - 10 of **10** on **switch or crossbar with filter and select signals and CAM**. (0.38 seconds

Concurrent searching of different tables within a content addressable memory
US Pat. 6744652 - Filed Aug 22, 2001 - Netlogic Microsystems, Inc.
11. corresponding circuitry of the cross-bar **switch** for translat- „ Pr°g/am ...
decoders to **select** one or tially **select** a first row of **crossbar switch** 430. ...

Concurrent searching of different tables within a content addressable memory
US Pat. 6967855 - Filed Aug 11, 2003 - NetLogic Microsystems, Inc.
10 illustrates memory storage cell 1100 that is one Block **filter** register ...
processor provided on the output string to the **CAM** block. The bar **switch** 430. ...

Highly automated agricultural production system
US Pat. 4015366 - Filed Apr 11, 1975 - Advanced Decision Handling, Inc.
If this embodiment control 83 permits **switch** 72 to place any of the **cam-** is ...
crossbar switch could be used in which aperture and **filter** selection. ...

Highly automated agricultural production system
US Pat. RE31023 - Filed Mar 2, 1979 - Advanced Decision Handling, Inc.
For instance, an electro-mechanical **crossbar switch** could be used in which ...
4 by 87, are closed by a common **select** magnet (not shown) on the **switch** ...

Keyboard signal storage mechanism
US Pat. 4235554 - Filed Nov 16, 1978 - Silver Seiko Limited
The **cam** follower stud 68 of bellcrank 70 is now engaged with the other low ...
that is, the end of the rotation of the **cam** 67 with the **filter** shaft 50. ...

Content addressable memory having column redundancy
US Pat. 7016243 - Filed Oct 11, 2004 - NetLogic Microsystems, Inc.
In response to the CC **signals**, steering circuits 120 and 122 steer data access
... 1) to **select** one or more rows of **CAM** cells 202 for writing or reading. ...

Content addressable memory having column redundancy
US Pat. 6714430 - Filed May 10, 2002 - NetLogic Microsystems, Inc.
In response to the CC **signals**, steering circuits 120 and 122 steer data access
... 1) to **select** one or more rows of **CAM** cells 202 for writing or reading. ...

Content addressable memory having column redundancy
US Pat. 6804135 - Filed Feb 9, 2004 - NetLogic Microsystems, Inc.
In response to the CC **signals**, steering circuits 120 and 122 steer data access
... 1) to **select** one or more rows of **CAM** cells 202 for writing or reading. ...

VACUUM **FILTER**
US Pat. 3332553 - Filed Oct 29, 1964
Operation of clean -**filter** web After closure of the **switch** 128 by the timer TD-2,
the Bite-ring continues until the vacuum eventually builds up to the value ...

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Patent Search Patents **1 - 10** of **33** on **switch with select signal and bit mask and content addressable memor**

Concurrent searching of different tables within a **content addressable memory**
US Pat. 6744652 - Filed Aug 22, 2001 - Netlogic Microsystems, Inc.
11 **memory** storage cell type. If the **memory** storage cell stores ... of let an
input **signal** IN on one of the **signal** lines 1030(0)- cross-bar **switch** 430 by ...

Content addressable memory with selectable mask write mode
US Pat. 6757779 - Filed Oct 31, 2001 - NetLogic Microsystems, Inc.
A **content addressable memory** (CAM) device comprising: a CAM array; ... 1 wherein
the **select** circuit includes a **select** input to receive a mode **select signal**, ...

Concurrent searching of different tables within a **content addressable memory**
US Pat. 6967855 - Filed Aug 11, 2003 - NetLogic Microsystems, Inc.
The **memory** storage cells may be programmed In one embodiment, ... -bar **switch**
430 by activating the signals on **signal** lines -1030(nl) to pass to one of the ...

Bit level programming interface in a **content addressable memory**
US Pat. 6993622 - Filed Feb 15, 2002 - NetLogic Microsystems, Inc.
For example, **switch** 430 may be configured to **select** particular bytes of input
string 405 transmitted on **signal** lines 435 and load them into certain byte ...

Hardware-assisted fast bank-swap in a **content-addressable-memory (CAM)** processor
US Pat. 6549442 - Filed Jul 25, 2002 - NeoMagic Corp.
For example, one **signal** can cause the masker to drive the compare **bit** lines and
... first mux 92 is sent a **select signal** that corresponds to index C, ...

Sequentially comparing **content addressable memory**
US Pat. 6215685 - Filed Nov 30, 1998 - Texas Instruments Incorporated
switch 25e, one of each associated with each row of tag **memory** 20. ... 6, tag
memory 20 has **bit- select** lines 17 like those of the cells 10 and ...

Content addressable memory with programmable word width and programmable priority
US Pat. 6934795 - Filed Oct 31, 2001 - NetLogic Microsystems, Inc.
The class code **mask** values may be provided together with the class code 6010 ...
A **content addressable memory** (CAM) device comprising: a CAM array having a ...

Content addressable memory with power reduction technique
US Pat. 6646899 - Filed Sep 21, 2001 - Broadcom Corporation
10 Instead, power is consumed in the ASID CAM 44 at a context **switch** or after
... [n]) and the **mask bit** as well. The compare line generator circuits 72A-72B ...

Content addressable memory with power reduction technique
US Pat. 6785152 - Filed Aug 14, 2003 - Broadcom Corporation
Instead, power is consumed in the ASID CAM 44 at a 15 context **switch** or after
... [n]) and the **mask bit** as well. The compare line generator circuits 72A-72B ...

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Patent Search   Patents **1 - 10** of **45** on **switch or crossbar with select signals and CAM**. (0.69 seconds)

Concurrent searching of different tables within a content addressable memory
US Pat. 6744652 - Filed Aug 22, 2001 - Netlogic Microsystems, Inc.
11. corresponding circuitry of the cross-bar **switch** for translat- „ Pr°g/am ...
decoders to **select** one or tially **select** a first row of **crossbar switch** 430. ...

Concurrent searching of different tables within a content addressable memory
US Pat. 6967855 - Filed Aug 11, 2003 - NetLogic Microsystems, Inc.
... and **crossbar switch** 720 is programmed by program data (PDATA) to **select** ...
4A), and output **signals** Y represent filtered output string provided to a **CAM** ...

Hardware-assisted fast bank-swap in a content-addressable-memory (**CAM**) processor
US Pat. 6549442 - Filed Jul 25, 2002 - NeoMagic Corp.
The index **signals select** one of the registers in register files 102, 104, ...
the desired bank(s) using a **crossbar switch** or multiplexers, or routing logic. ...

Bit level programming interface in a content addressable memory
US Pat. 6993622 - Filed Feb 15, 2002 - NetLogic Microsystems, Inc.
4A illustrates one embodiment of a **CAM** device 20 having translation circuitry.
... For example, **switch** 430 may be configured to **select** particular bytes of ...

DAVIDSON
US Pat. 2740841 - Filed Jan 16, 1953
call. ;lt¡ :that the release of relay :301 also releases :the associated relay
306 and the **crossbar switch select** .magnet '304. vit will also be noted that ...

Receive processing for dedicated bandwidth data communication **switch** backplane
US Pat. 6314106 - Filed Apr 20, 1998 - Alcatel Internetworking, Inc.
8, output queue 440 is arranged as a 45 rotating **crossbar** which includes ...
Signals cam grnt n indicate whether arbiter 420 has granted a request issued by ...

Receive processing for dedicated bandwidth data communication **switch** backplane
US Pat. 6931019 - Filed Jun 1, 2001 - Alcatel
8, output queue 440 is arranged as a rotating **crossbar** which includes multiplexors
... **Signals cam** grnt n indicate whether arbiter 420 has granted a request ...

MESSAGE TRANSMISSION SYSTEM
US Pat. 2871286 - Filed Nov 18, 1953
18 to prevent the response thereof to the **signals** which follow and ... 60 circuit
permission to use the **crossbar switch**, it operates **select** magnet 3-01 and ...

MACHINE TOOL CONTROL SYSTEM
US Pat. 3125796 - Filed May 13, 1960
The control circuit for storing information in the cross-bar **switch** registers
... concerned with the storage of coded information in the **crossbar** registers. ...

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Patent Search

Patents **1 - 10** of **639** on **switch or crossbar with select signals**. (0.38 seconds)

Cascadable bus based crossbar switch in a programmable logic device
US Pat. 6590417 - Filed Apr 3, 2001 - Cypress Semiconductor Corporation
Line control **select signals** 55 108.0 through 108.15 are each delivered by **select**
line controllers 109.0 through 109.15, respectively. 50 **Crossbar** circuit ...

Sampled data filtering system, including a crossbar switch matrix, as for a ghost cancellation system
US Pat. 4727424 - Filed Oct 16, 1986 - RCA Corporation
In the system described above, the **switch** elements change state one column at a
time as each of the three column **select signals** is strobed. ...

Queue size arbitration method and apparatus to enhance performance of crossbar cell switch
US Pat. 6717945 - Filed Jun 19, 2000 - Northrop Grumman Corporation
allowing the **crossbar** cell **switch** to be implemented with smaller, ... 1-5 may be
used with the addition of the latch function or the row **select** logic 5 336. ...

Crossbar switch with distributed memory
US Pat. 4973956 - Filed Dec 22, 1988 - General Electric Company
NOR gate 19 has two input ports connected to a row **select** line rsi and to a
diagonal **select** line ... When barrel shift or **crossbar switch** operations are to ...

Large scale interconnecting switch using communication controller groups with multiple input-to-one output signal lines and adaptable crossbar unit using plurality of selectors
US Pat. 5832303 - Filed Aug 17, 1995 - Hitachi, Ltd.
and **select signals** 6151-61532 having a transmission device address ... A **crossbar**
part used in a large-scaled interconnection **switch** which interconnects a ...

Digital crossbar switch
US Pat. 4852083 - Filed Jun 22, 1987 - Texas Instruments Incorporated
There are 4 different inputs to multiplexer 66 the selection of which is controlled
by multiplexer control **select signals**, ...

Apparatus and method for establishing construction information of a non-management ethernet switch
US Pat. 6502160 - Filed Mar 20, 2000 - LG Information & Communications Ltd.
T , . . , , . . , , **crossbar switch** in a case that a first option board is dis- ...
a bus chip **select** signal CS together with input/output **signals** DI **switch** ...

Programmable port for crossbar switch
US Pat. 5734334 - Filed Aug 17, 1995 - I-Cube, Inc.
The output buffer 34 is disabled by CLOCK **signals** were last concurrently asserted,
setting multiplexer 82 to **select** the output of distate buffer In the ...

Instruction cache associative crossbar switch
US Pat. 6360313 - Filed Sep 8, 2000 - Intergraph Corporation
The decoder **select** signal enables only the appropriate set of ... This **signals**